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[Download free pdf] Weaving High Performance Multiprocessor Fabric: Architectural Insights to the Intel QuickPath Interconnect

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R. Maddox, G. Singh, R. Safranek, R. Colwell : Weaving High Performance Multiprocessor Fabric: Architectural Insights to the Intel QuickPath Interconnect before purchasing it in order to gauge whether or not it would be worth my time, and all praised Weaving High Performance Multiprocessor Fabric: Architectural Insights to the Intel QuickPath Interconnect:

1 of 1 people found the following review helpful. Good introductory bookBy J. MeyerAn expensive introductory book on Intel's proprietary QuickPath chip to chip interconnect technology (the Intel version of AMD's hypertransport). I got a couple of concepts from the book but not worth the price. It covers a little bit about all of the SW/HW layers but I was mainly interested in the PHY layer which this book had very little info about. Sits gathering dust on my bookshelf.

Welcome to the era of the Intel QuickPath Interconnect!Weaving High Performance Multiprocessor Fabric is written for hardware design, validation and BIOS engineers to introduce the compelling mix of performance and features in the Intel QuickPath Interconnect. From the foreword, Robert P. Colwell opines: "Even for inveterate geeks like me, most technical books are dry as dust and work much better than insomnia pills. They should come with warning stickers: Do not operate heavy machinery for a week after reading this book. Not this book though: Weaving High Performance Multiprocessor Fabric is engaging, educational, well-organized and directly useful. It doesn't get any

better than that." It explains the Intel QuickPath Interconnect, which provides the foundation for future generations of Intel microprocessor systems with a high-speed, packetized, point-to-point system interconnect that uses multiple narrow high-speed links to stitch together processors and IO hubs into a fabric of a distributed shared memory-style platform architecture. "With one day of reading this book, everyone familiar with the existing Front Side Bus architecture will have good visibility into what is new in the Intel QuickPath Interconnect." - Simon Czermak - Fujitsu Siemens Computers Ltd